A Simplified Model Of QuickCheck Automata∗

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ABSTRACT
Placing guarantees on a program’s correctness is as hard as it is essential. Several approaches to verification exist, with testing being a popular, if imperfect, solution. The following is a formal description of QuickCheck finite state automata, which can be used to model a system and automatically derive command sequences over which properties can be checked. Understanding and describing such a model will aid in integrating this verification approach with other methodologies, notably runtime verification.

1. INTRODUCTION
Verifying a program’s correctness is often non-trivial, and sometimes outright impossible. Several approaches towards verification have been adopted over time, including testing [1] and runtime verification [3]. The aforementioned techniques are concerned with the verification of a partial or full program with respect to a given property, where a property characterises a set of expected behaviours in some manner, as will be discussed shortly.

Testing always involves the observation and classification of a sample of executions [2]. One would typically provide a series of test inputs, possibly generated automatically, which are then executed and evaluated. Any discrepancies between the result of an execution and that which was expected indicate that there is a fault. The expected output can be characterised by a property expressed in several ways, such as through a list of input-output pairs or an automaton describing the system’s behaviour. A common shortcoming of testing is that the test sample used is rarely exhaustive, leaving certain code paths untested and leading to the persistence of latent errors.

QuickCheck [5] is an automatic test case generation and execution tool developed by QuviQ, which facilitates the testing of Haskell or Erlang programs. In broad terms, QuickCheck ensures that a property (a code block that returns true on success) holds over a set of test cases. Test values are initialised using generator functions, which allow a user to specify the structure of a property’s input data. The randomness is derived from the distribution of values produced by the generators. QuickCheck also allows different quantifiers to be used. For example, one could easily check a property \( P(n) \) such that \( \forall n : n \cdot P(n) \). Naturally, only a subset of the possible values of \( n \) will be checked, meaning that the property will not be tested exhaustively over all inputs, yet confidence in the system will increase as the number of analysed test cases grows.

QuickCheck also allows one to model a system as a finite-state automaton, from which different program execution paths can be derived and tested automatically. This paper will focus on the definition and use of such automata, starting with an informal description of their behaviour illustrated by a simple example, followed by a formal definition of a simplified version of the automaton.

2. QUICKCHECK AUTOMATA
QuickCheck supports the creation of a class of finite-state automata [5], henceforth abbreviated as QCFSA. A QCFSA describes some aspect of the system by modelling a set of possible function call sequences, or program traversals. Each QCFSA transition corresponds to a function call. QuickCheck traverses the automaton, choosing arcs at random, and generates a sequence of calls (a trace). The trace can then be executed, and its result may be checked within a QuickCheck property.

Every arc has an associated precondition and postcondition function, which returns a boolean value. The precondition function is used to limit the generation of certain traces, with a path not being chosen whenever its precondition fails. A state may have multiple outgoing arcs for the same function call, provided that not more than one precondition returns true at that point in the traversal for that call. The postcondition function is checked while a trace is being run, following the execution of the transition’s associated function. If the postcondition returns anything other than true, then the trace is considered to be a failing test.

A QCFSA may also store state data, which can be updated during a transition by associating a next state data function. This function is invoked after a postcondition evaluates to true, and returns the next state’s data based on the current state data and the result returned by the function once it terminates. Additional auxiliary functionality, namely the ability to weight transitions, will not be considered in this exposition.

2.1 An Example
Consider a simple controller operating a light, which can either be on or off. The light may be manipulated by calling one of two functions, switch_on() and switch_off(), which return the light’s new state.

Assuming that the light is initially off, this system could be modelled using a two-state automaton, as illustrated in Figure 1. This automaton could be written as a QuickCheck automaton as follows:
The light switch model was tested extensively using the automaton as defined previously. QuickCheck did not succeed in finding an error-inducing counterexample, which suggests (but does not prove) that the program correctly mirrors the model. So as to induce an error, an invalid implementation of the switch_on() function was tested. Instead of returning an on state, the function was set to return an error. QuickCheck succeeded in finding a failing trace, represented symbolically as

\[
\{\text{set}, \{\text{var}, 1\}, \{\text{call}, \text{light}, \text{switch}_\text{on}, []\}\},
\{\text{set}, \{\text{var}, 2\}, \{\text{call}, \text{light}, \text{switch}_\text{off}, []\}\}\]

This was then minimised to a single call, namely

\[
\{\text{set}, \{\text{var}, 1\}, \{\text{call}, \text{light}, \text{switch}_\text{on}, []\}\}\]

3. **FORMALISING QCFSAs**

The following section presents a formal description of a simplified model of QCFSAs. The model is simplified in that it assumes that all operations are free from side-effects. It also assumes that functions are invoked without any parameters, which in practical terms would require that arguments are passed through a global state data structure.
Throughout the text, the term “event” refers to a symbolic call corresponding to an implemented function in the system under test. The automata being considered are assumed to be deterministic, using the definition provided in Section 3.1.1.

### 3.1 The Model

If \( \Theta \) represents a system state, a QCFSA \( Q \) is described by a tuple \( \langle Q, q_0, \Sigma, \theta_0, \text{run}, \rightarrow \rangle \), where

\[
\begin{align*}
Q & \text{ is a set of states} \\
q_0 & \text{ is the initial state} \\
\Sigma & \text{ is an alphabet of events representing functions in the system under test} \\
\theta_0 & \text{ is the initial global system state containing all of the program’s state variables, including } Q \text{’s local state data} \\
\text{run} & \in \Sigma \rightarrow \Theta \rightarrow \Theta \text{ is a function which returns the updated system state on executing a given event’s associated function with the specified system state data} \\
\rightarrow & \subseteq Q \times (2^\Theta \times \Sigma \times 2^\Theta \times \Theta \rightarrow \Theta) \times Q \text{ is a transition relation}
\end{align*}
\]

The transition relation \( \rightarrow \) serves to associate the preconditions, postconditions and state-changing actions to be performed on calling an event. Pre- and postconditions are characterised by sets of valid system states. Thus, a precondition is satisfied if the current system state is a member of the set. Similarly, a postcondition is satisfied if the system state entered after executing an event’s associated function is a member of the set of valid postcondition states. As mentioned previously, a transition may specify a state-changing action, which is primarily used for updating the automaton’s private state data. This action is performed whenever a postcondition executes successfully.

Moving from state \( q \) to \( q' \) on event \( a \) with system state sets \( \text{pre} \) and \( \text{post} \) and executing state-changing action \( \alpha \) is denoted by

\[
q \underbrace{\text{pre}}_{a} \underbrace{\text{post}}_{\alpha} \rightarrow q' \Rightarrow (q, \text{pre}, a, \text{post}, \alpha, q')
\]

which is simply a more elegant way of representing a transition.

#### 3.1.1 Determinism

The model describes automata that are deterministic. Given any pair of transitions emanating from a state, either

- the transitions share the same state changing action, pre- and postconditions and lead to the same state, implying that the transitions are identical, or
- the transitions have non-intersecting preconditions, meaning that for each event, only at most one transition can be pursued

Formally,

\[
\forall a, q', q_1, q_2, \text{pre}_1, \text{pre}_2, \text{post}_1, \text{post}_2, \alpha_1, \alpha_2: \\
\{(q', \text{pre}_1, a, \text{post}_1, \alpha_1, q_1) \in \rightarrow \wedge \} \\
(q, \text{pre}_2, a, \text{post}_2, \alpha_2, q_2) \in \rightarrow \Rightarrow (q', \text{pre}_2 \wedge \text{post}_1 = \text{post}_2 \wedge \alpha_1 = \alpha_2 \wedge q_1 = q_2) \lor \\
(q', \text{pre}_1 \wedge \text{pre}_2 = 0)
\]

### Example 3.1.1

The light-switch model described in Section 2.1 can be formalised as

\[
\langle Q, \text{off}, E, \{\{\text{light}, \text{off}\}\}, \text{run}, \{\{S, \text{pre}, e, \{\text{post}\}, \lambda : \text{post}\}, S'\} | \text{pre} \in 2^\Theta, \theta \in 2^\Theta, S \in Q, S' \in Q, e \in E, \\
\text{post} = \text{run}(e, \theta) \wedge \{\{\text{light}, S'\}\} = \text{post} \rangle
\]

where

\[
Q = \{\text{off}, \text{on}\}, \\
E = \{	ext{switch_on}, \text{switch_off}\}, \\
\text{run} = \lambda e, t: \\
| \{e = \text{switch_on} \rightarrow \{\text{light}, \text{on}\}\} \\
| \{e = \text{switch_off} \rightarrow \{\text{light}, \text{off}\}\}
\]

### 3.2 Configurations

#### 3.2.1 Single Events

The configuration of a QCFSA consists of a pair \( (q, \theta) \), where \( q \) is the current state and \( \theta \) is the global system state at that point in the execution. Performing a step from a configuration \( (q, \theta) \) to a valid configuration \( (q', \theta') \) on executing an event \( a \) is denoted by \( (q, \theta) \xrightarrow{a} (q', \theta') \), and is possible if

\[
\exists \text{pre}, \text{post}, \alpha, \theta_m: \\
q \underbrace{\text{pre}}_{a} \underbrace{\text{post}}_{\alpha} \rightarrow q' \wedge \\
\theta \in \text{pre} \wedge \\
\theta_m = \text{run}(a, \theta) \wedge \\
\theta_m \in \text{post} \wedge \\
\theta' = \alpha(\theta_m)
\]

A transition leads to a bad configuration if its postcondition is violated once an event is executed. As described earlier, a postcondition is violated if the new state is not part of the valid system state set. Moving to a bad configuration under event \( a \) is thus defined as

\[
(q, \theta) \xrightarrow{a} \backepsilon \subseteq \text{pre}, \text{post}, \alpha, a': \\
q \underbrace{\text{pre}}_{a} \underbrace{\text{post}}_{\alpha} \rightarrow a' \wedge \\
\theta \in \text{pre} \wedge \\
\text{run}(a, \theta) \notin \text{post}
\]

#### 3.2.2 Event Sequences

Trivially, a configuration does not change if no event is executed. Thus, if \( \epsilon \) is the null event,

\[
(q, \theta) \xrightarrow{\epsilon} (q', \theta') \equiv q = q' \wedge \theta = \theta'
\]

Given a sequence of events \( \alpha : s \), where \( s \) can itself be another sequence, moving from one configuration to another valid configuration whilst executing the sequence can be defined recursively as

\[
(q, \theta) \xrightarrow{\alpha} (q', \theta') \equiv (q', \theta') \equiv (q'', \theta'') \equiv (q''', \theta''') \equiv (q', \theta')
\]
This result assumes that any two consecutive steps can be combined into a single step over a sequence. Conversely, a sequence can be decomposed at any point into two sub-sequences. This can be described formally using the equality $\exists_{q_1, q_2}. (q_1, q_2) \Rightarrow \emptyset$.

A system cannot move to a bad configuration on an empty event, that is,

$$(q, \emptyset) \not\Rightarrow \emptyset$$

Moving to a bad configuration given a sequence of events is defined as

$$(q, \theta) \not\Rightarrow \emptyset \iff \exists q', \theta'. (q, \theta) \Rightarrow (q', \theta') \not\Rightarrow \emptyset$$

with the last step leading to a bad configuration.

Example 3.2.1
With reference to the light-switch model described in Section 2.1, the following expressions hold

$$(\text{off}, \{\text{light}, \text{off}\}) \overset{\text{switch on}}{\Rightarrow} (\text{on}, \{\text{light}, \text{on}\})$$

If 

$$\text{switch on}()$$

is implemented incorrectly and does not return

$$(\text{off}, \{\text{light}, \text{off}\}) \overset{\text{switch on}}{\Rightarrow} \emptyset$$

3.3 Describing Traces
Using the aforementioned definitions, one may characterise two important sets related to the traversal of an automaton $Q$, these being the set of negative traces $N(Q)$ and the set of testable traces $T(Q)$.

$N(Q)$ is the set of traces which, starting from the automaton’s initial configuration, lead to a bad configuration. Thus,

$$N(Q) \equiv \{ w : \Sigma^* | (q_0, \theta_0) \not\Rightarrow \emptyset \}$$

$T(Q)$ refers to the set of traces with which the automaton is concerned, that is, the traces which it tests. Thus,

$$T(Q) \equiv \{ w : \Sigma^* | \exists q, \theta. (q_0, \theta_0) \Rightarrow (q, \theta) \} \cup N(Q)$$

The task of checking whether or not a trace $w$ is valid with respect to a property described by an automaton $Q$ can thus be reformulated into a membership problem. Trace $w$ would be valid as long as $w \not\in N(Q)$. In general, a program could be considered as being valid with respect to $Q$ if it can never produce a trace in $N(Q)$ when starting from the initial conditions set by $Q$.

4. QUICKCHECK AND RUNTIME VERIFICATION
By introducing the concept of the set of negative traces for an automaton $Q$, parallels can be drawn between QuickCheck automata and runtime verification monitors. In simple terms, runtime verification is concerned with checking whether a given trace (partial in the case of online monitoring) is a member of the set of valid program behaviours, defined by one or more monitors [4]. Often, a monitor describes the invalid program behaviours, and thus one checks for non-membership. Thus, to translate a QCFSA $Q$ into a runtime monitor, one would have to define a monitor which recognises $N(Q)$.

Providing the ability to translate between QCFSA and runtime monitors (and possibly back) would aid verification by allowing testing to continue beyond the system’s development phase. It would also allow one to test critical parts of a system exhaustively whilst delaying the verification of non-essential components until runtime. Alternatively, one may switch between testing and verifying a component at runtime based on the computing resources available, increasing confidence in a system by testing it during lulls in processor demand. The translation could also incorporate details or statistics obtained during testing in order to guide runtime verification and minimise the number of unnecessary or repeated checks.

Based on preliminary efforts in translating between QCFSA and runtime verification monitors, it appears that most of the issues encountered stem from the fact that QuickCheck operates as a generator, whilst runtime verification listens on a stream of events that is generated by an external entity. This may lead to problems related to non-determinism and the ordering of operations related to a transition.

5. CONCLUSIONS AND FUTURE WORK
This paper has presented an overview of QuickCheck automata, illustrating their operation through an example and providing a formal description of a restricted automaton. It also defined the notion of an automaton’s set of testable and negative traces.

The model detailed in this text is restricted primarily in that it is deterministic and assumes that functions have no side-effects. A global system state serves in preserving the model’s computational capabilities whilst mitigating the problems that arise from a lack of function arguments. Some of the model’s restrictions may have to be lifted in order to facilitate the integration of this approach with other testing techniques.

6. REFERENCES